Technical Seminar of IEEE Consumer Electronics Society Bombay Section Chapter (in association with 23rd VDAT 2019)

July 4 - 6, 2019
Venue: Hotel Radisson Blu, Indore

Advisory Committee, VDAT 2019 - Dr. Anirban Sengupta, Founder & Chair of IEEE Consumer Electronics Society Bombay Section Chapter

VLSI SOCIETY OF INDIA

Keynote Speakers

Energy Efficient Embedded Memories in Advanced CMOS: Trends and Prospects

Jaydeep Kulkarni University of Texas, Austin

System Architectures and Machine Learning – hype or HYPE?

Preeti Ranjan Panda IIT Delhi

Soft Error in Chips and Systems

Krishnan Rengarajan Cypress Semiconductor

4C's of AI

Sreekant Prudvi

Avera Semi

Industry Speakers

Next Generation IoT and Automotive SoC Design: Challenges and Opportunities

Bedanta Choudhury NXP Semiconductors

Virtual Prototyping of System on Chip (VSoC) using Transaction Level Modeling

Nishit Gupta MeitY, Govt. of India

Analog and Mixed Signal Trends in SoC Development

Prashant Aadmane Tessolve Semiconductor

Automotive Functional Safety using Self-Test Mechanism like LBIST and MBIST

Sandeep Bhattacharya ST Microelectronics

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Industry Speakers

Who's Driving? The Responsibilities that come with Providing Physical IP to the Automotive Market

Amit Chhabra
ARM

Design Automation in Custom Layout Design

Bhupendra Vishvakarma
Intel

Advancement and New Trends in DFX Domain to Cater Needs of Semiconductor Industry
Ramesh Devani
einfochips

Embedded Deep Learning for Energy Constrained Systems

Manuj Ayodhyawasi
ST Microelectronics

Industry 4.0 Myths

Preet Yadav
Wipro

Functional Safety in Automotive Applications

Amit Agarwal
nVIDIA

FPGA and Adaptive Compute Acceleration Platform for AI Applications

Vaibhav Kale
Xilinx

22FDX Technology - Ultra Low Power Design Solution for AI Application

Suman Dwivedi
Globalfoundries